

S/N 10/046,783



AF/2839
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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Dong Zhong et al.

Examiner: Neil Abrams

Serial No.: 10/046,783

Group Art Unit: 2839

Filed: January 17, 2002

Docket: 884.978US1

Title: A GRID ARRAY SIGNAL CONDUCTING ARRANGEMENT (AS AMENDED)

Assignee: Intel Corporation

Customer No. 21186

**NOTICE OF APPEAL FROM THE DECISION OF THE EXAMINER
TO THE BOARD OF PATENT APPEALS AND INTERFERENCES**

MAIL STOP AMENDMENT

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

In compliance with 37 C.F.R. § 41.31(a)(1), Applicants hereby appeal to the Board of Patent Appeals and Interferences from the decision dated December 6, 2004, of the Examiner rejecting claims 1-26 of the above-identified patent application.

A request for an extension of time to respond to the Examiner's rejection is submitted herewith along with payment of the required extension fee.

Please charge the amount of \$500.00 in payment of the Notice of Appeal fee under 37 C.F.R. § 41.20(b)(1), as well as any additional required fees, to Deposit Account No. 19-0743.

Respectfully submitted,
DONG ZHONG ET AL.

By their Attorneys,
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Date

June 6, 2005

By

Ann M. McCrackin
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CERTIFICATE UNDER 37 C.F.R. 1.8:

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to MAIL STOP AMENDMENT, Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on June 6, 2005.

Name

John D. Gustin-Wrathall

Signature

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